

POLITECNICO DI MILANO

## Integrated photonics (The need to peep light)

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## **Photonic Devices Lab**

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life.augmented





http://photonics.deib.polimi.it

PHOTONPATH

PoliMi micro- nanotechnology center





650m<sup>2</sup>



High Resolution Electronic Measurements in Nano-Bio Science 2025 - A. Melloni

HUAWEI

## >25 years of Multidisciplinary photonics@ PoliMi









## **Photonics**

A Key Enabling Technology for Europe

The European Technology Platform Photonics21 represents the photonics community of industry and research organisations. Jointly with the European Commission our members develop and implement a common photonics strategy in a Horizon2020 **Public Private Partnership (PPP)** to spur growth and jobs in Europe.

#### Photonics21 – Photonics PPP Annual Activity Report 2017



## Europe's age of light!

How photonics will power growth and innovation

Download from: https://www.photonics21.org

·020-203L



## Photonics is pervasive

2.	Pho	18	
	2.1	Information & Communication	18
	2.2	Industrial Manufacturing & Quality	35
	2.3	Life Science & Health	41
	2.4	Emerging Lighting, Electronics & Displays	49
	2.5	Security, Metrology & Sensors	60
	2.6	Design and Manufacturing of Components & Systems	70
	2.7	Education, Training & Disruptive Research	63

#### **Photonics** A Key Enabling Technology for Europe

EPIC

PHOTONIC

The European Technology Platform Photonics21 represents the photonics community of industry and research organisations. Jointly with the European Commission our members develop and implement a common photonics strategy in a Horizon2020 **Public Private Partnership (PPP)** to spur growth and jobs in Europe.

European Photonics Industry Consortium















## Polifab - the micro and nano fabrication facility of PoliMI



Polifab provides high technological standards for a wide range of applications: photonics, micro and nanoelectronics, MEMS, biotechnologies, advanced materials and nanotechnology. Polifab acts as an **aggregating** center for academic researchers, start-ups and companies



+600 mg cleanroom, ISO 06



Up to 200 mm wafers





11 staff members





Open access



+160 researchers in 2024



- ~ 25 M€ tools installed
- ~ 40% from JRC with STm

Multidisciplinary approach, flexibility

## **Polifab** facilities





#### Lithography

Photolithography

2 Maskless photolitho Electron beam lithography 50keV Thermal scanning lithography

Deposition

evaporation

ALD

Electron beam

Etching

Thermal evaporation Sputtering for metals and oxides Chemical vapor deposition Electrodeposition

Reactive ion etching Deep RIE Ion beam etching Plasma ashing Wet etching Lift-off

MoS:

Motol

#### Characterization

Electrical

Micro-Raman

spectroscopy

EDX

Metal

Backend Optical microscopy Stylus profilometry Annealing ovens Optical profilometry Rapid thermal SEM, AFM Ellipsometry

annealing Wire bonding Dicing measurements Pick & Place X-ray diffraction Alignements tools Two-photon polymerization tool













Photonic Devices Group - PoliMI 2025

#### POLITECNICO DI MILANO

(short) Overview on integrated photonics

- The photonic chip as system
  - Monitors
  - Actuators
  - ✓ (Feedback and Control)

Applications: routing, tuning and computing

### 1969: 56 years of integrated optics ...





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# Sin cladding rate of photonics: the ingredients



## WAVEGUIDE





## Waveguides...





















## Materials for Photonic circuits





## **Integrated Photonics Building Blocks**





## Integrated photonics: towards ubiquitousness





## The need of Integrated Photonics







## **Photonic Integration: Motivation**



- Greatly reduced component cost
  - Monolithic interconnection of device elements
  - Simpler packaging and assembly, standard processes
- High reliability
  - Less interfaces
- High functionality
  - Many more functional elements per chip, higher creativity in design
- High phase stability, excellent device matching
  - Permits interferometric structures
- Robust
  - Single chip designs with minimal optical interfaces are ideal for demanding environments
- Higher power efficiency
  - Minimize optical power loss at interfaces between device elements
- Reduce packaging costs
- Co-integration with electronic, microfluidic, MEMS, microwave, ....
- Volume production

## Submarine cables, optical fibers





## Why optical fibers?

- **Data Rate:** Optical fibers support **Tbps speeds**, copper cables are limited by high frequency metallic loss (**no skin effect**)
- Longer Distance: Optical fibers can transmit data over many km without significant signal loss.
- Power Consumption: Optical transceivers consume less power per bit than copper alternatives, making them more energyefficient

Optical fibers generate **less heat** than high-speed copper cables.

- Latency: Optical signals travel at near the speed of light (both in fibers and copper cables !... See table) – Hollow core fibers?
- Electromagnetic Interference (EMI): optical fibers are not affected by EM noise, ensuring signal integrity in high-density environments (like thousands of servers)
- Space & Weight: Optical fibers are thinner and lighter than copper cables, better airflow and cooling





Material	Dielectric constant	VOP
	TYPICAL INSULA	TION MATERIALS
Cellular TFR	1.38	85%
FEP	2.1	69%
Silicone	3.6-2.1	53-69%
TFE	2.1	69%
Polyethlene	2.3	66%
PVC	8.2-3.0	35-58%
Nylon	4.5-3.6	47-53%
Optical fiber	2.1	70%

Velocity of  
propagation (VOP)  
$$VOP[\%] = \frac{v}{c}$$

c speed of light (vacuum)









#### Today

#### **Optical Transceivers**

Convert electrical signals from GPUs, CPUs, and switches into optical signals for high-speed data transmission.

400G–800G today, moving toward 1.6T optical links in near future.



#### **Optical Fibers** Today

Polymer waveguides on carrier

Intra-Rack Communication (< 2 m)  $\rightarrow$  Multimode fibers replacing copper cables Inter-Rack Communication (< 50 m)  $\rightarrow$  Multimode fibers Inter-cluster Networking (100 m - 10 km)  $\rightarrow$  Single-mode fibers Inter Data Center Links (50 km - 1000+ km)  $\rightarrow$  Single-mode fibers

Optical Switches (Silicon Photonics & DWDM) Tomorrow (< 5 years) Use photonic switching to manage massive AI workloads more efficiently than electrical packet switches

- <50ns switching time (faster than electrical packet switches).
- No electrical-to-optical conversion overhead (direct optical switching).







### Silicon Photonics: roadmap and markets



#### 2021-2027 SILICON PHOTONIC DIE FORECAST BY APPLICATION

Source: Silicon Photonics 2022 Report, Yole Intelligence, 2022



#### **Optical «processors» (photonic computing)**

- Reduce latency, power consumption, and bandwidth bottlenecks
- Integration of optical components (lasers, waveguides, modulators, photodetectors) directly into GPUs, TPUs...
- Optical interconnects between GPUs and memory connections (inside servers)
- Use light for (some) AI computations (photonic accelerators) → Lower energy, faster speeds
- Quantum photonic processors



Researchers Develop Novel Analog Processor for High Performance Computing August 30, 2021

Aug. 30, 2021 — Analog photonic solutions offer unique opportunities to address complex computational tasks with unprecedented performance in terms of energy dissipation and speeds, overcoming current limitations of modern computing architectures based on electron flows and digital approaches.





#### Billions of \$ of investments.... (< 10 year old company )



https://lightmatter.co/

#### https://ayarlabs.com/



https://www.celestial.ai /



https://www.psiquantum.com/



Operation	Energy per bit		
Wireless data	10 – 30µJ	<b>10-6</b> Transmit a signal (bit) across a	
Internet: access	40 – 80nJ	requires charging/discharging t	
Internet: routing	20nJ	capacity of the electrical link	
nternet: optical WDM links	3nJ	$10^{-9}$ I $C \approx 1$ pF/cm	
Reading DRAM	5рЈ		
Communicating off chip	1 – 20 pJ	Average length of the lin	
Data link multiplexing and timing circuits	~ 2 pJ	10-12 $1  mm - 1  cm$	
Communicating across chip	600 fJ	$C \approx 0.1 - 1  \text{pF}$	
Floating point operation	100fJ		
Energy in DRAM cell	10fJ	Energy per bit (1 V)	
Switching CMOS gate	~50aJ — 3fJ	$\mathbf{IU}^{-1}\mathbf{J} = \mathbf{E}^{-1}\mathbf{U}^{-1}\mathbf{J}$ $\mathbf{F} \approx \mathbf{C}\mathbf{V}^{2}$	

 $\rightarrow$  Energy required to transmit data across electronic chips has not scaled down much in the last years

→ Energy consumption of (super)computers is mainly due to short-distance signal transmission inside electrical chips

David A. B. Miller, "Attojoule Optoelectronics for Low-Energy Information Processing and Communications», J. of Lightwave Technology 35(3), Feb. 2017

## Energy consumption of hyperscale computing

a Computing power demands

10<sup>23</sup> FLOP/Day



Supercomputers can achieve **hundreds of peta FLOPS** (floating point operations per second) in processing data

```
# FLOP/s = 10^{18} FLOP/s (# FLOP/day \approx 10^{23})
```

Energy per FLOP & Communication  $E_{FLOP} = 1 - 10 \text{ pJ}$ Total power consumption >10 MW

#### New computing technologies ( $\rightarrow$ «Silicon» Photonics)

- Remove (or reduce) energy consumption associated with data trasmission and processing in digital electronics
- Transmit and compute in the optical domains



Attojoule Optoelectronics for Low-Energy Information Processing and Communications

David A. B. Miller, Fellow, IEEE, Fellow, OSA

D.A.B. Miller, "Attojoule Optoelectronics for Low-Energy Information Processing and Communications», J. of Lightwave Technology 35(3), Feb. 2017

Computing performance (petaFLOP days)





Matrix-vector multiplication (MVM) & multiplications & accumulation (MAC) is everywhere in data processing:

- convolution, factorization, linear equalization, filtering, Fourier transform...
- neuro-inspired computing (weighted interconnections between adjacent photonic neurons)

Advantages of<br/>photonic computing- high speed (THz) & low latency (< ns) in solving linear mathematical operations</th>- low energy/bit consumption (<< pJ/bit)</td>

 $\rightarrow$  optical computing is a competitive candidate for **artificial intelligence accelerators** 

 $\rightarrow$  acceleration is achieved by matching math operations and photonic hardware

## Photonics Integration – Optical interconnect





## Toward a system...

## Monitor (Phase) Actuators Feedback and control

## Control layer for photonic integrated circuits





## Light monitors: Ge, InP, hybrid, monolithic...



### Ge on Silicon



#### **(b)** S.I.-InP substrate n+ InP Cathode Cathode -InGaAs p+ InGaAs Au Prob Anode SIO

AIN substrate

(a)





**III-V** compounds





#### 4.2% Ge-Si Lattice mismatch

- $\Rightarrow$  specific growth strategies required
- $\Rightarrow$  growth on thin SiGe buffers
- $\Rightarrow$  multi step growth process
- $\Rightarrow$  thermal annealing (reduce dislocation density)

#### Butt coupling

Si

## Actuators





## Phase / Amplitude

- Fast (MHz for tuning and reconfiguration)
- **Compact** (10-100 µm)
- Low Power consumption (< mW)

Permanent, self holding to avoid to continuous feed Analog / Digital

## Thermal actuators, mature technology, power hungry

M. R. Watts, et al. Opt. Lett. 38, (2013)



Si channel waveguide with embedded Si heater (n-doped)



## Integrated optical actuators



#### **Thermal actuators**

M. R. Watts, et al. Opt. Lett. 38, (2013)



Si channel waveguide with embedded Si heater (n-doped)





W.M. Green et al., Opt. Express 15 (2007)

#### **MEMS based switches**



S. Han et al., Berkeley, (2015)

## Phase-change materials

C. Rãos *et al*, Nature Photonics **6** (2015) A. Joushaghani et al, APL, 102, 061101 (2013)

#### Plasmonic memristor



C. Hoessbacher et al., Optica 1 (2014)

#### Graphene, MoTe<sub>2</sub>, ITO modulators



R. Amin et al., arxiv (2018)

## Integrated optical actuators



### Thermal field induced by heater heater under cladding Silicon substrate



#### **MEMS based switches**





#### **Plasmonic memristor**



#### Graphene, MoTe<sub>2</sub>, ITO modulators



## **Electronics** at service of photonics





## Photonic Building blocks

## Integrated power splitters









$$T_{C} = \begin{bmatrix} \cos(\kappa L) & -j\sin(\kappa L) \\ -j\sin(\kappa L) & \cos(\kappa L) \end{bmatrix} = \begin{bmatrix} r & -it \\ -jt & r \end{bmatrix}$$

## Key building blocks



#### Mach Zehender interferometer (MZI)



#### **Microring resonator (MRR)**



## Mach-Zehnder interferometer (unbalanced)



#### feed-forward interferometer



## **Ring Resonator Filter**



#### feed-back interferometer

- Same frequency resonse as a Fabry-Pérot cavity
- Lossless case ( $\alpha = 0$ ),  $|H_{tr}(f_0)|^2 = 0$  if  $r_1 = r_2$

• Lossless (
$$\alpha = 0$$
) & symmetric ( $r_1 = r_2 = r$ )  $B_{3dB} \cong FS$ 



α Attenuation constant frequencies

## APPLICATIONS

## (just the taste...)

111111



TiN

## Reconfigurable filters



## Reconfigurable hitless filter





CLIPP at the Drop port to read optical label Mach-Zehnder Modulators (MZM) in the add port to apply optical label

um

## Biosensing and integrated photonics







#### Evanescent field detection Negligible absorbtion Phase change of the light

Phase-intensity conversion with an "interferometer" (ring resonator)

## Biosensing and integrated photonics





## **Photonic Computing**

#### Forbes

#### Optical Computing: What It Is, And Why It Matters

	-	
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	1 222	all all

Kazuhiro Gomi Forbes Councils Member Forbes Technology Council COUNCIL POST | Membership (Fee-Based)

Sep 10, 2024, 09:30am EDT

 Kazuhiro Gomi, NTT Research CEO. Leads research in physics & informatics, cryptography & information security, medical & health informatics.



https://www.forbes.com/councils/forbestechcouncil/2024/09/10/ optical-computing-what-it-is-and-why-it-matters/

#### Positioning & Ranging (LIDAR)



- Electronic computing... two decades ago, GPUs were starting to supplant CPUs
- Moore's law is fading
- The **von Neumann** computing model (memory separated from processing since the 1940s) has inherent limits
- To address big computing challenges, it helps to think big and in new ways...
- Use photons instead of electrons... (somewhere)
- A general-purpose optical neural network may be years out... but could end up being 1,000 times as efficient as its electronic counterparts

## Beam forming, imaging & microscopy



Micro-projectors & augmented reality



Quantum photonics (communication, computing & sensing)











... è una interferenza ( > sommatore coerente)



Let's consider an optical signals (frequency, polarization and spatial mode here is not much relevant) with power  $P_1$ 



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### Summing... amplitude or power?

Let's consider 2 optical signals (same frequency, polarization and spatial mode) with power  $P_1$  and  $P_2$  respectively

Is it possible to get the signal "sum" with field amplitude  $E_{out} = E_1 + E_2$ ?

Can we implement a field «amplitude adder»? NO !!!

A simple justification
$$P_1 \propto |E_1|^2$$
 $E_1 = 1$  $P_1 = 1$  $P_2 \propto |E_2|^2$  $E_2 = 1$  $P_2 = 1$ There is no way to make an «electric field adder» $E_{out} = E_1 + E_2 = 1 + 1 = 2P_{out} = 4$  $\overbrace{out} instead \dots P_{in} = P_1 + P_2 = 2$ 





Let's consider 2 optical signals (same frequency, polarization and spatial mode) with power  $P_1$  and  $P_2$  respectively

How to get the signal "sum" with power  $P_{out} = P_1 + P_2$ ?

Can we use a «power combiner»... like a directional coupler?

 $E_{out,1} = E_{out,1} = \frac{1}{\sqrt{2}} \begin{bmatrix} 1 & -j \\ -j & 1 \end{bmatrix} = E_{out,2} = -j\frac{1}{\sqrt{2}}E_1 + \frac{1}{\sqrt{2}}E_2 = -j\frac{1}{\sqrt{2}}E_1 + \frac{1}{\sqrt{2}}E_2$  $E_1$ 3dB\*  $|E_1| = |E_2| \longrightarrow P_1 = P_2$ The output power depends on the  $P_{out.1} = P_1 + P_2$ amplitude and the phase of the input ... only if  $\angle E_1 - \angle E_2 = -\frac{\pi}{2}$ signals !(\*)  $P_{0112} = 0$ 

(\*) using non-3dB couplers, phase dependence will remain anyway !

 $P_1 \propto |E_1|^2$  $P_2 \propto |E_2|^2$ 

... that's interference ( $\rightarrow$  coherent adder)





 $\lambda_1 \lambda_2$ 



Interference may happen only if signals share frequency, polarization and spatial mode)

Use different frequencies/wavelengths (non-coherent adder)

 $P_1$  $P_2$ 

 $\begin{array}{c} P_1 & \underline{\lambda}_1 \\ P_2 & \underline{\lambda}_2 \end{array}$ 

λ

MUX -

 $\lambda_1 \lambda_2 \lambda_N$ Different frequencies mean that the signals are orthogonal in the frequency domain

 $\lambda_2$ 

 $P_2$ 

 $I_{out} \sim P_{out} = |E_1 \cos(\omega_1 t) + E_2 \cos(\omega_2 t)|^2 = |E_1|^2 + |E_2|^2 + ... \cos(\omega_1 - \omega_2) + ... \cos(\omega_1 + \omega_2)$ 





 $P_{out}$ 





The photonic circuit implement a NxM linear transformations W (matrix)

Matrix-vector multiplication (MVM) is one of the fundamental mathematical operations widely used in data "processing":

- convolution, factorization, linear equalization,
- Neural networks (weighted interconnections between adjacent neurons)

$$y_n = \sum_{m=1}^M w_{n,m} x_m$$

It basically translates in doing **multiplications** of optical signals (*x*<sub>i</sub>) with weigths (w<sub>ii</sub>) and **sums** them up

#### *How to implement it optically?*



$$Y = WX = \begin{bmatrix} w_{11} & w_{12} & \dots & w_{1N} \\ w_{21} & w_{22} & \dots & w_{2N} \\ \dots & \dots & \dots & \dots \\ w_{N1} & w_{N2} & \dots & w_{NN} \end{bmatrix} \begin{bmatrix} x_1 \\ x_2 \\ \dots \\ x_N \end{bmatrix} \qquad y_n = \sum_{m=1}^M w_{n,m} x_m$$



A filter bank is used to scale the various elements (multipliers)

**PDs** detetect only the optical power (not the complex amplitude)

#### In non coherent MVM weigths are real numbers

J. Cheng et al., « Photonic Matrix Computing: From Fundamentals to Applications », Nanomaterials, 11, 1683 (2021)





- (e.g., the brain has 10<sup>11</sup> neurons and 10<sup>15</sup> synapses)
- Distributed processing based on weighted interconnections & localized nodes having simple logic
- Nonlinearity required to implemented decision making and model complex systems





2x2 Unitary Transformation

- $U^{\dagger}U = UU^{\dagger} = I$  ,  $U^{-1} = U^{\dagger}$  , det(U) = 1
- preserves inner product  $\langle Ux, Uy \rangle = \langle x, y \rangle$ , i.e.,
- preserves "length" & "angle" (only a "rotation") ⇒ power conservation
- eigenvalues on unit circle, orthogonal eigenvectors





Condition to null the output power at port 2

$$y_{2} = -je^{j\theta/2} \left[ \cos\left(\frac{\theta}{2}\right) x_{1} - e^{j\phi} \sin\left(\frac{\theta}{2}\right) x_{2} \right] = 0$$
  
$$\Rightarrow \cos\left(\frac{\theta}{2}\right) x_{1} = e^{j\phi} \sin\left(\frac{\theta}{2}\right) x_{2} \qquad \Rightarrow \begin{cases} \theta = 2\tan^{-1} \left|\frac{x_{1}}{x_{2}}\right|, \quad \theta \in [0, \pi] \\ \phi = (\angle x_{1} - \angle x_{2}) + 2N\pi \end{cases}$$





$$\phi_i = \angle x_{i+1} - \angle x_i$$
$$\theta_i = 2\tan^{-1} \left| \frac{x_i}{x_{i+1}} \right|$$

- Combining 2x1 coherent adders, an arbitrary number of inputs signals can be coherently summed
- Calibration strategy: zeroring the power at monitor ports

#### Free Space Optical (FSO) Link





#### The Optical Analog Processor based receiver



 $P_{out} = \sum_{i=1}^{n} |s_i|^2$ 



A device capable of coupling an arbitrary free-space beam (non-spatially coherent  $\rightarrow$  "multimode") into a specific output beam (spatially coherent  $\rightarrow$  ""single-mode")







W. Bogaerts, ... F. Morichetti, A. Melloni Programmable photonic circuits, Nature 586, 207–216 (2020)



Any matrix can be factorized as the product of two unitary matrices V and U, and a diagonal matrix  $\Sigma$  (singular values)

 $W = V \Sigma U^{\dagger}$ 

A system of MZI meshes can be used to implement any arbitrary matrix W



... but also to calculate the SVD of an arbitrary optical sistem (SVD factorization)



### Application: Chip to chip communication





Establish multiple orthogonal communication channels through arbitrary scatterer N antennas... up to N spatial **orthogonal** modes  $\rightarrow$  Towards free-space optical MIMO









The <u>best FSO</u> <u>orthogonal</u> modes (channels) can be found <u>automatically...</u>

## Obstacle in the free-space path







This is a physical implementation of **singular value decomposition (SVD)** of the channel transmission matrix



Applications: general purpose, Matrix Algebra, mathematical accelerators, convolutional NN, tensor cores,...

